

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	7505576
<b>Application Number:</b>	10595157
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	3496
<b>Title of Invention:</b>	Method of Manufacturing Multi-Layer Circuit Board
<b>First Named Inventor/Applicant Name:</b>	Toshiaki Takenaka
<b>Customer Number:</b>	52349
<b>Filer:</b>	Andrew Lawson Dunlap/ann leveille
<b>Filer Authorized By:</b>	Andrew Lawson Dunlap
<b>Attorney Docket Number:</b>	2006-0223A
<b>Receipt Date:</b>	28-APR-2010
<b>Filing Date:</b>	09-MAR-2006
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<b>Application Type:</b>	U.S. National Stage under 35 USC 371

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## File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/ Message Digest	Multi Part /.zip	Pages (if appl.)
1	Request for Continued Examination (RCE)	AttachA_RCE_P04030802.pdf	57209	no	1
			d7249c43ed713540a842495679a5af0ccd3e24d		

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### Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	31909	no	2
			4e61565be3e6cd0f94cc22237f13e3c0b0f8a72e2		

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**If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.**

#### **National Stage of an International Application under 35 U.S.C. 371**

**If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.**

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